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MAY 02 2005

In re the Application of

Applicant Vikas K. Agrawal, et al.

Docket Number: TI-35282

Serial No.: 10/663,575

Art Unit: 2824

Filed: 09/16/03

Examiner: Van Thu T. Nguyen

For: Cycle Ready Circuit for Self-Clocking Memory Device

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<input type="checkbox"/> CONTINUATION APP'N	<input type="checkbox"/> ELECTION
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NAME OF INVENTOR(S): Vikas K. Agrawal, et al.	
TITLE OF INVENTION: Cycle Ready Circuit for Self-Clocking Memory Device	
TI FILE NO.: TI-35282	DEPOSIT ACCT. NO.: 20-0668
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For: CYCLE READY CIRCUIT FOR SELF-CLOCKING MEMORY DEVICE

Commissioner of Patents
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AMENDMENT

In response to the Official Action in this case mailed
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